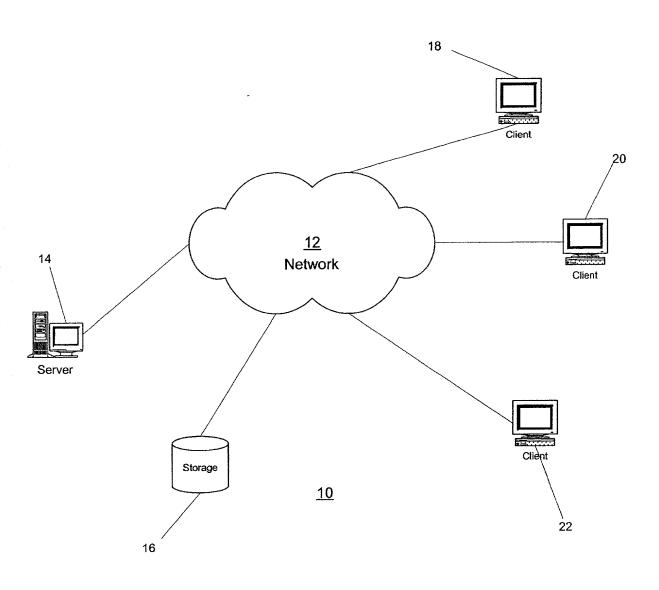
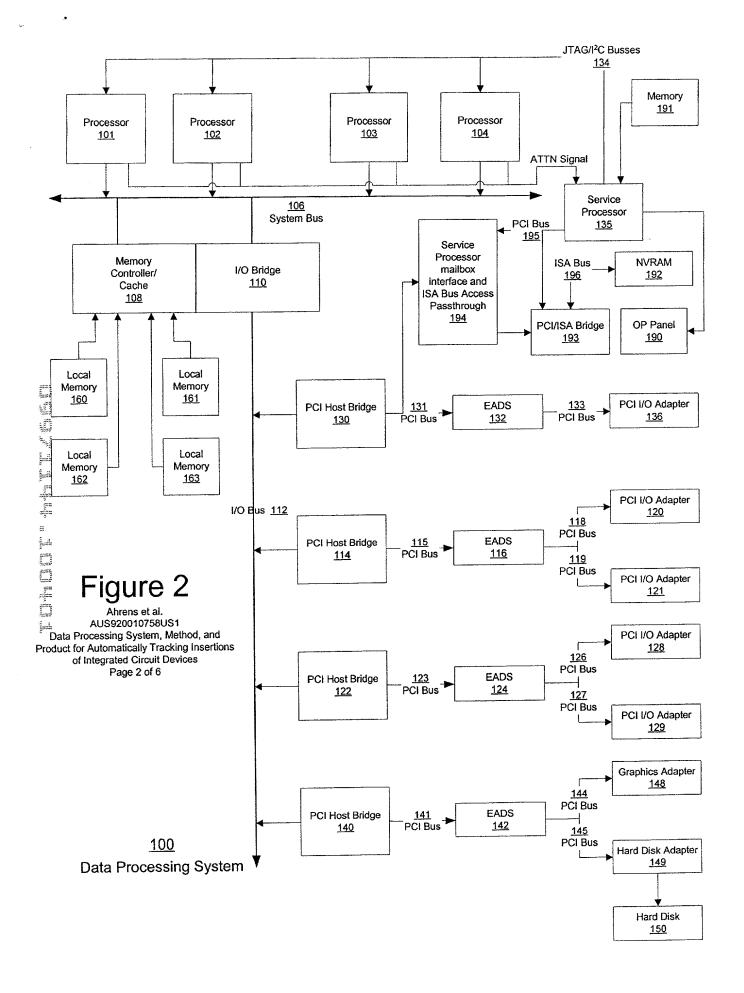
Figure 1

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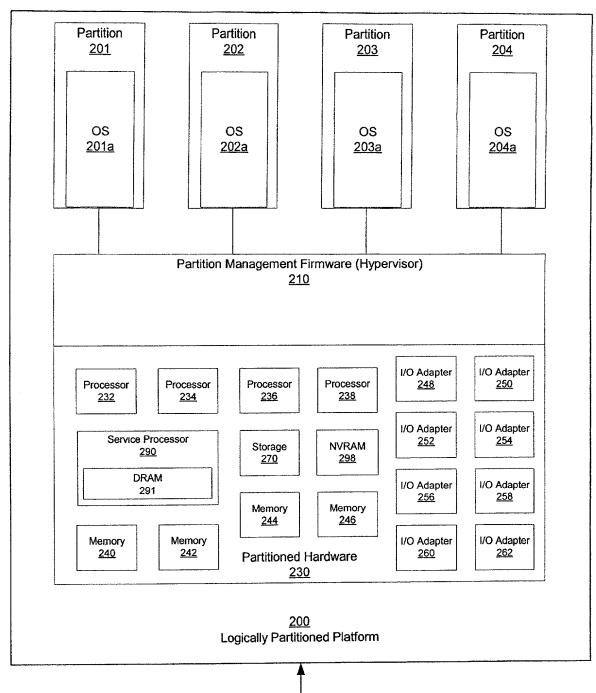


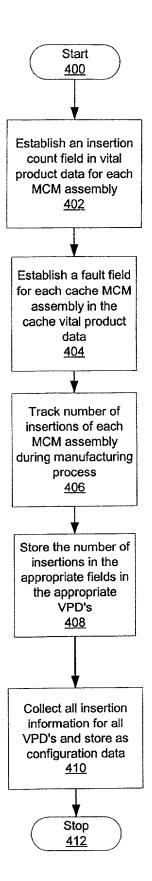
Figure 3

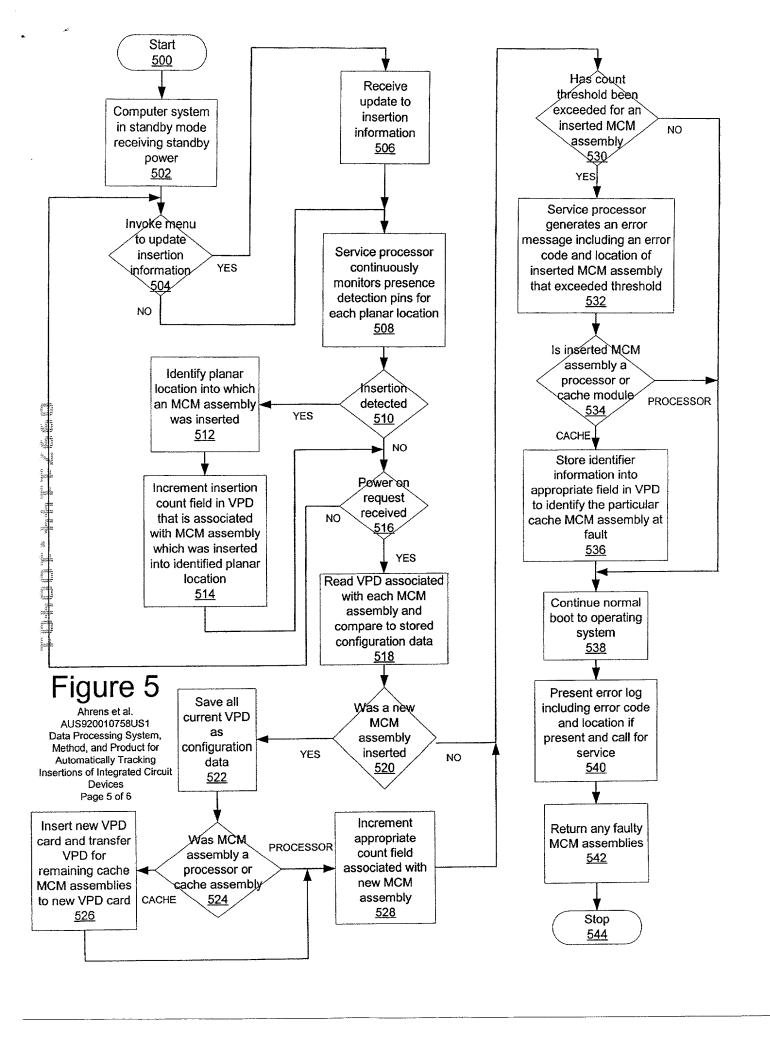
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Hardware System Configuration Machine <u>299</u>

Figure 4

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Processor 1-8	U1.3-P1-C1			
		7		
Processor 9-16	U1.3-P1-C2			
MCM Assembly Name	Location Code	Number of Times Inserted	Fault	Other fields
L3 Module 1	U1.3-P1-C5	3	0	
L3 Module 15	U1.3-P1-C19	10	1	
L3 Module 16	U1.3-P1-C19	1	0	
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	L3 Module 1	L3 Module 1 U1.3-P1-C5 L3 Module 15 U1.3-P1-C19 L3 Module 16 U1.3-P1-C19	L3 Module 1 U1.3-P1-C5 3 L3 Module 15 U1.3-P1-C19 10 L3 Module 16 U1.3-P1-C19 1	L3 Module 1 U1.3-P1-C5 3 0 L3 Module 15 U1.3-P1-C19 10 1 L3 Module 16 U1.3-P1-C19 1 0 Figure 6 Ahrens et al.

Figure 6

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